Piecewise-Linear Modeling of Analog Circuits
Based on Model Extraction from Trained Neural Networks

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Abstract

This paper presents a new technique for automatically creating analog circuit models. The method extracts piecewise linear models from trained neural networks. A model is a set of linear dependencies between circuit performances and design parameters. The paper illustrates the technique for an OTA circuit for which models for gain and bandwidth are generated. As experiments show, the obtained models have a simple form that accurately fits the sampled points. These models are useful for fast simulation of systems with non-linear behavior and performances.

1 Introduction

The need for mixed analog-digital designs is predicted to dramatically increase over the next years [1] [9]. The digital part of mixed-signal systems can be efficiently designed with a low effort using modern high-level, logic-level and physical-level design automation tools. In contrast, there is a lack of systematic design methods and efficient general-purpose synthesis environments for analog circuits [1] [9]. As a result, analog designs continue to seize a considerable portion of the total design time for mixed-signal systems [8] [9]. There is a persistent need for developing improved methods and tools to level the design productivity and quality of analog circuits. This paper presents an original analog circuit modeling method that can be efficiently employed for both circuit design and synthesis.

Analog circuit models (macromodels) express mathematical relationships between significant electrical and geometrical parameters of a circuit (like device sizes, layout parasitics, signal frequencies, noise etc) and specific performance attributes (such as circuit gain, bandwidth, power consumption, slew rate, harmonic distortion etc) [9]. For example, it is customary to formulate mathematical equations for an op amp gain and bandwidth as functions of transistor sizes and the value of the compensating capacitor [13]. Models are key components for both manual design and automated synthesis of analog circuits. A designer uses circuit models to capture the relevant dependencies in a design, and then find the design parameters (i.e. device sizes) according to the performance requirements that need to be satisfied [13]. Circuit synthesis tools use circuit models to improve the effectiveness of the exploration process and speed-up their convergence towards optimal solutions [14] [20]. In both cases, models must accurately capture the behavior of circuits without increasing the complexity of their mathematical expression [14].

Circuit models are very important for speeding up the convergence of simulation-based circuit synthesis tools. It has been reported that one of the important challenges is the large number of optimization variables that must be simultaneously tackled [11] [14]. This poses challenges to traditional exploration-based synthesis methods, which need a very long time to complete their search, or might even not converge towards a good solution [11]. A solution to this problem is to use models to speed up synthesis by guiding the search towards attractive solution space regions [6]. Most of the time, models are used to quickly find the performance attributes of the explored designs. Periodically, exhaustive circuit simulations are performed to correct the inaccuracies introduced by the models. Even for very sensitive designs such as RF mixer circuits, it has been shown that performance estimation through a combined circuit model evaluation and circuit simulation offers good accuracy levels while significantly reducing synthesis time [20].

Another important application of circuit models is for top-down design and synthesis of analog systems [5]. Top-down system synthesis proceeds in two steps [5]: the first step (called architecture generation) explores alternative architectures for a system expressed at an abstract level. The second step (named constraint generation) allocates performance constraints to each block in an architecture so that the overall system performances are optimized. Note that an abstract perspective on the analog circuits is maintained during both synthesis steps. As a result the constraints allocated to a block might be very difficult to be obtained with real analog circuits (for example large gains for large circuit bandwidths, high slew rates etc). Also, important aspects such as noise and layout parasitics are neglected during system design. Circuit models help eliminating these limitations by providing knowledge on (1) circuit performance trade-offs, (2) feasibility ranges for circuit performances, and (3) impact of physical-level elements such as noise and layout parasitics on circuit performances.

Circuit modeling techniques fall into two categories: (1) physical modeling methods and (2) mathematical modeling techniques [14]. Physical modeling methods simplify a circuit to a reduced sub-circuit that includes only the dominant devices...
in the circuit. Such models are useful in offering a qualitative insight into the circuit but are limited in offering also a quantitative perspective. Models can be successfully used for circuit analysis but not for device sizing, circuit optimization and synthesis. Mathematical models capture quantitative relationships between the parameters and performances of a circuit. However, these models might not have any connection to the physical structure of the circuit. Non-linear regression methods are traditionally used to produce mathematical models [10] [3] [4].

The main limitation is that for a large number of data points, it is very difficult to find a single mathematical formula that accurately fits all points [14].

This paper presents a new technique for extracting piecewise linear models from trained neural networks. A model is a set of linear dependencies between circuit performances and design parameters. Dependencies are valid over a range of the parameters. Section 3 presents for an OTA circuit [13] the extracted models for gain and bandwidth as functions of frequency and layout parasitics. As experiments show, the produced piecewise linear models have a simple form that accurately fits the sampled points. Moreover, piecewise linear models are a promising method for approximating nonlinear behavior and performances with a small error [12]. There are powerful simulation methods that use piecewise linear models to quickly calculate system performances [12]. Our work addresses the need for a method to systematically create piecewise linear models used for simulation [12].

The model generation techniques starts with the step of training a neural network. A backpropagation algorithm is used for training until the desired accuracy is obtained at the output of the network. Next, a pruning method is applied to eliminate the neurons with insignificant contributions to the model. The size of the network is thus reduced without significantly affecting the modeling accuracy. Then, the sigmoidal activation function of each neuron is approximated with a piecewise linear function that includes three linear segments. Finally, the piecewise linear functions for input, hidden and output neurons are composed together to generate the final model of a circuit. The function composition algorithm is based on automatically expressing linear equations and inequalities for the neurons. Equations are then solved to find the feasibility (input) domain for each linear segment in the model.

The paper includes six sections. Section 2 presents related work on modeling with neural networks, and highlights the main contributions of this paper. Section 3 offers a theoretical description of the modeling problem. Section 4 presents the algorithm for extracting piecewise-linear models from trained neural networks. Section 5 illustrates the models generated for an OTA circuit. Finally, we put forth our conclusions.

2 Related Work on Modeling with Neural Networks

Neural networks have been successfully used in various types of problems, including classification and function approximation. They are able to learn any type of nonlinear mapping based on their well known property of universal approximators. The main problem of neural networks consists in their opaque representation of the knowledge embedded in the parameters of the model. Due to the nature of processing that takes place in a neural network - parallel distributed processing among connected neurons - it is very difficult to interpret what a neural network does.

Extracting symbolic knowledge out of a neural network would improve the understandability of the generated rules.

3 Problem definition

The task is to approximate the nonlinear function represented by a trained feedforward neural network with a piecewise linear mapping. The neural network considered here has three layers: an input layer $I$, a hidden layer $H$ and an output layer $O$. The goal of the extraction method is to find a set of $L$ linear models each of the following form: $L = \{a_1 x_1 + a_2 x_2 + \ldots + a_l x_{l} \mid l = 1 \ldots L, a_{ij} \in \mathbb{R}\}$, where $x_i$ is the output of a neuron in the $I$ layer. The region in the input space where model $l$ is valid is defined by a set of constraints of the following form: $C^l = \{c_{m} x_{1} \mid c_{m} \in \mathbb{R}\}$, where $M^l$ is the number of constraints for model $l$.

The model $l$ is active if all the constraints in $C^l$ are satisfied for a set of input values: $\{x_1, \ldots, x_l\}$ and inactive if at least one of the constraints is violated. The region in the input space where a constraint set $C^l$ is satisfied is called the valid region of model $l$. All constraint sets $C^l$ must satisfy the following requirements:

1. The valid regions of any pair of linear models must not intersect in any point in the input space: $C^p \cap C^r = \emptyset$, for $p \neq r$.
2. The set of constraints in $C^l$ is minimal. By removing any constraint, the valid region for model $l$ changes.
4 Model Extraction Method

The neural networks considered here are three layer feed-forward networks. There are \( N \) input neurons in the \( I \) layer, \( H \) hidden neurons in the \( H \) layer and \( O \) output neurons in the \( O \) layer. The weight matrix between the input and the hidden layer is \( W^{IH} = \{ w_{ij}, j = 1 \ldots H, \ i = 1 \ldots N + 1 \} \), where \( w_{ij} \) is the weight of the connection between the input neuron \( i \) and the hidden neuron \( j \). The input layer and the hidden layer are augmented with a bias neuron. The weight matrix between the hidden and the output layer is \( W^{HO} = \{ w_{kj}, k = 1 \ldots O, \ j = 1 \ldots H + 1 \} \) with \( w_{kj} \) the strength of the connection between output neuron \( k \) and hidden neuron \( j \).

The activation function of the hidden and output neurons is the sigmoidal function \( \phi(x) = \frac{1}{1 + e^{-x}} \), with \( 0 < \lambda \leq 1 \). The weighted sum at the input of a hidden neuron and at the input of an output neuron are respectively:

\[
\begin{align*}
\sum_{i=1}^{N} w_{ij} x_i + 1, & \quad \sum_{i=1}^{H} w_{kj} x_j + 1, \\
\end{align*}
\]

where \( x_i \) is the output of the input neuron \( i \). The output of the hidden neuron \( j \) is \( x_j = \phi(h_j) \), and the output of the \( x_k \) neuron is \( x_k = \phi(h_k) \).

First, the network is trained with the backpropagation algorithm [15] until the desired mean square error on the training and validation data sets is reached. Second, a pruning technique is applied to eliminate the insignificant weights.

The pruning repeatedly removes the most insignificant weight from the remaining weights until a stopping criteria is satisfied. The significance of a weight is proportional with the reduction in accuracy on both training and validation data. The accuracy reduction is measured iteratively as:

\[
\Delta \text{MSE}(r) = \text{MSE}(r - 1) - \text{MSE}(r),
\]

where \( \text{MSE}(r) \) is the mean square error on the training and validation data of the neural network at step \( r \) of the pruning process. The weight that produces the least reduction in accuracy at each step is eliminated. The stopping criteria is the total loss of accuracy, which has to be smaller than a maximum limit. If a weight between a hidden and an output neuron is eliminated then the hidden neuron and all the weights between the input layer and the hidden neuron are also eliminated. The pruning is necessary in order to reduce the number of linear models.

The extraction algorithm starts with the pruned network. The idea is to approximate the nonlinear sigmoidal activation function with a piecewise linear function. One way to do this is by dividing the input space into three regions: for small \( x \) values the number of linear models.

The next step consists in finding the the linear models and the regions in the input space where they are valid. For each region, a linear function in the input variables is generated by substituting the sigmoidal activation function \( \phi(x) \) with the linear one \( g(x) \) in \( x_k = \phi(h_k) \). The coefficients of the linear functions depend on the weight values:

\[
x_k = \phi(\sum_{i=1}^{N} w_{kj} \phi(\sum_{i=1}^{N} w_{ij} x_i)) \approx g(\sum_{i=1}^{H} w_{kj} g(\sum_{i=1}^{N} w_{ij} x_i))
\]

The main steps of the linear model extraction method are as follows:

1. Linearization of the hidden neurons. Extract and refine the sets of constraints \( C^{0r} \), \( h = 1 \ldots H \), \( r = 1 \ldots R \), with \( R \) the number of linear regions of \( g(x) \). A set \( C^{0r} \) represents the region where the output of the hidden neuron \( j \) is given by \( x_j = g(h_j) \), with \( g(\cdot) \) the \( r \) branch of the linear function \( g(x) \). The refining process of a set of constraints \( C^{0r} \) is described below.

2. Evaluation of the weighted sum input to the output neurons (\( h_k \)). Find all valid combinations of linear regions in the hidden neurons of the following form:

\[
O^p = \{ r_1^p, r_2^p, \ldots, r_p^p \}, \quad r_j^p \in \{ 1, 2, \ldots, R \}
\]

and \( p = 1 \ldots R^H \). For each such combination, the output of all hidden neurons is completely specified, and therefore \( h_k \) can also be evaluated. The region in the input space where combination \( O^p \) is valid is defined by the constraint set:

\[
\mathcal{C}^p = C^{1^1} \cap C^{1^2} \cap \ldots \cap C^{1^H} \cap \mathcal{C}^p
\]

The process of intersecting sets of constraints is detailed below. A combination \( O^p \) is valid if its constraint set \( \mathcal{C}^p \) delimits a nonempty region in the input space. The valid constraint sets \( \mathcal{C}^p \) are refined as in step 1.

3. Linearization of the output neuron. For each valid combination region defined by \( \mathcal{C}^p \), express the output of the network as a linear combination of input variables. Depending on the range of values for \( h_k \) - the weighted sum input to output neuron \( k \) for combination \( p \) - the activation function of the output neuron can be split into one or more linear regions (\( g_k(h_k^p) \)). The maximum number of linear models that can be generated is \( (R^H + 1) \). The set of constraints that define the validity region of a linear model is defined by:

\[
\mathcal{C}^p = C^{1^1} \cap C^{1^2} \cap \ldots \cap C^{1^H} \cap \mathcal{C}^p
\]

The graph of the sigmoidal function (\( \phi(x) \)) and of the approximated piecewise linear mapping (\( g(x) \)) is shown in Figure 1.

Figure 1: The graph of the sigmoidal function (\( \phi(x) \)) and of the approximated piecewise linear mapping (\( g(x) \)).
The method for linear model extraction is applied to model the frequency response of an analog circuit for different parasitic levels. The data is obtained using SPICE simulations for the analog circuit for a number of frequency \((F)\) and parasitic \((P)\) values: \(F = T\) and \(P = 9\). Correspondingly, there are \(FP\) gain values.

The two inputs - frequency \((f)\) and parasitics \((c)\) - are first scaled: 
\[
x_1 = \frac{\log(f) - \mu}{s\sigma},\quad \text{with } \mu \text{ the average and } s\sigma \text{ the standard deviation.}
\]
Similarly the parasitics input is also scaled \((x_2)\).

The output - the gain \((g)\) - is translated \((a_g)\) to fall inside the range of the sigmoidal activation function.

With these transformations, the data is split into a training (85%) and a test set (15%). A three layer neural network is trained and the best performance on the training set is obtained for a neural network with \(H = 7\) hidden neurons. There are \(I = 2\) input neurons one for frequency \((x_1)\) and one for parasitics \((x_2)\) and one output neuron - the gain. Figure 5 shows the simulation gain compared to the unscaled output of the trained neural network for four values of the parasitics. The network output approximates well the simulation data.

The trained network is pruned. From the initial set of weights between the input and hidden neurons \((I + 1)H = 21\) eight weights are eliminated. Because one of the hidden neurons gets disconnected completely from the input layer - all its weights were deleted - one hidden neuron is removed. The loss in accuracy for the pruned network is 4.19% of the original network mean square error.

The pruned network is linearized according to the procedure described in Methods section. First, the sets of constraints - \(C^p\) - for all hidden neurons are found. Not all sets are valid, meaning that not all hidden neurons have output in all possible linear regions of \(g(x)\). Second, the constraint sets \(C^p\) are obtained by intersecting the \(C^P\) sets corresponding to combination of linear regions \(O^P\). From the initial number of combinations \(H^H = 729\) only 18 are possible - all hidden neurons have solutions in the linear regions specified in each combination. Then, the 18 combinations are checked for validity and
Different parasitic values. The extracted piecewise linear model approximates the gain frequency plots of a simulated analog circuit for different parasitics values. First, a neural network is trained to approximate the nonlinear mapping of the simulation outputs, and with the linear models, the error between the nonlinear neural network model and the piecewise linear one is small in most of the points, but there are some areas where the error is bigger. These areas correspond to bigger linearization errors between the sigmoidal function and the linearized one.

For example, one of the linear models is: \( x_o = 0.0201723x_1 + 0.127456x_2 + 1.17566 \). The region where this output function is valid is defined by the set of 14 constraints with refined limits shown in Figure 3. In total, there are 14 linear models, 10 of them with a variable linear dependency in the input variables and 4 of them with a constant output.

Figure 4 shows the comparative results obtained with the initial neural network and with the linear models. The error between the nonlinear neural network model and the piecewise linear one is small in most of the points, but there are some areas where the error is bigger. These areas correspond to bigger linearization errors between the sigmoidal function and the linearized one.

6 Conclusions

A method was developed to extract piecewise linear models to approximate the nonlinear frequency response of analog circuits for different parasitics values. First, a neural network is trained to approximate the nonlinear mapping of the simulation points. A method of extracting piecewise linear models from the neural network is proposed, where the activation function of the neurons is approximated with a piecewise linear mapping. The number of generated linear models is reduced by checking the validity of each possible solution. The number of constraints that defines a region in the input space corresponding to a linear model is also reduced by eliminating redundant constraints. The extraction method was used to approximate the gain frequency plots of a simulated analog circuit for different parasitics values. The extracted piecewise linear model loses a little in accuracy compared to the initial neural network, but it gains in interpretability. One way to improve the accuracy of the linear models is to use instead of constant regions 0 and 1, variable regions where the output varies linearly with the input. Another possibility is to use the upper and lower limits of the weighted sum input to a hidden neuron to define a customized linear mapping for each neuron. It might be that the weighted sum input to a hidden neuron has a very restricted range, in which case the error for most input values will be large if we use a fix linearization function. For example, if most of the time the input to a hidden neuron falls around \( \pm \gamma \) then the linearization error will be big. In this case it is better to find a more appropriate linearization mapping that reduces the error for that neuron.

References

Figure 3: Constraint set defining the validity region for a linear model.

\[
\begin{align*}
6.05483x_1 + 5.05147x_2 &< 0.437622, & 6.05483x_1 + 5.05147x_2 &> -2.39329 \\
x_1 &> -0.587616, & x_1 &\leq 0.39531 \\
x_2 &> -0.387462, & x_2 &\leq 0.373985 \\
-1.22925x_1 + 2.98139x_2 &< 1.40069, & -1.22925x_1 + 2.98139x_2 &> -1.64138 \\
-2.59169x_1 - 4.12291x_2 &< 0.572377, & -2.59169x_1 - 4.12291x_2 &> -0.920589 \\
0.547341x_1 + 1.21511x_2 &> -0.254318, & 0.547341x_1 + 1.21511x_2 &< 0.323216 \\
0.108931x_1 + 0.688272x_2 &< 0.231289, & 0.108931x_1 + 0.688272x_2 &> -0.223593
\end{align*}
\]


